WHAT IS CLAIMED IS:

1. A method comprising:

　　　　executing corresponding instruction threads in parallel as a leading thread and a trailing thread;

　　　　saving result from the instruction executed in the leading thread and result from the instruction executed in the trailing thread to memory;

　　　　comparing the results saved in memory;

　　　　committing a single set of instruction to a memory state based on the compared result; and

　　　　deferring external updates until completion of the step of committing.

2. The method of claim 1, wherein the saved result are saved as speculative.

3. The method of claim 1 wherein the instructions are epoch instructions..

4. The method of claim 3, wherein the corresponding instructions read by the leading thread during the epoch contains same value when the corresponding trailing thread loads occurs.

5. An apparatus comprising:

　　　means for executing parallel threads as a leading thread and a trailing thread;

　　　means for saving the executed threads in a memory;

　　　means for comparing the results saved in memory;

　　　means for committing a single set of thread to a memory state based on the compared result; and

　　　means for deferring external updates until completion of the step of committing.

6. The apparatus of claim 5 wherein the executed threads are epoch threads.

7. The apparatus of claim 6, wherein each epoch is executed twice.

8. The apparatus of claim 5 wherein the corresponding instructions read by the leading thread during the epoch contains same value when the corresponding trailing thread loads occurs.

9. The apparatus of claim 8 wherein the buffered threads are stored as speculative.

10. The apparatus of claim 9 wherein the single set is committed if the compare result matches.